# Ode to a childhood dream Cross-compiling GNU epsilon to the Commodore 64

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**GNU** Project

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# The Commodore 64 hardware — overview





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## The Commodore 64 hardware — overview

The VIC-II graphics chip





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The Commodore 64 hardware — CPU

The CPU is a MOS 6510, essentially identical to the 6502.

- A simple design from the late 1970s:
  - 8-bit;
  - accumulator CPU (A, two index registers X and Y, flags);
  - no multiplication, no division, no floating-point;
  - Low frequency:
    - 0.985 MHz (PAL version);
    - 1.023 MHz (NTSC version);
  - 16-bit address bus:
    - 64KB address space, and 64KB RAM
    - clunky "zeropage" addressing modes, unsuited to pointers;
  - memory-mapped I/O
    - (with bank switching);



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## The Commodore 64 hardware — multimedia

- SID audio chip (waveform), very nice for its time;
- VIC-II video chip:
  - hardware sprites (good !);
  - no linear framebuffer (very painful).
  - addresses 16KB out of the main 64KB memory
    - bank switching: any one of bank [0, 3].



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# The Commodore 64 hardware — other hardware

Quite reasonable keyboard (the computer is all in the keyboard).

Some hardware extensibility:

- Video interface: TV or dedicated monitor
- User port
- Memory-mapped ROM cartridges
- joystick, mouse, modem, printer
- secondary storage:
  - floppy drive
  - audio cassette interface



Demo

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## Motivation

#### Why all of this?



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## The Commodore 64 — how it "felt"



Commodore 64C (released 1986 — functionally identical to the 1982 model, updated motherboard making it cheaper to produce) This is the same model I owned (but I had no floppy drive).



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# Commodore 64 system software

- KERNAL "operating system" in ROM:
  - memory-mapped, shadowing the last 8KB of RAM (unless disabled);
- "BASIC V2" interpreter in ROM:
  - memory-mapped, shadowing 8KB more;
  - very, very limited: PEEK and POKE;
  - BASIC starter;



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# Extensible programming language

GNU epsilon:

- Extremely small core language  $\varepsilon_0$
- Powerful extension capabilities
  - automatically rewrite programs into  $\varepsilon_{0}$
  - only have to compile  $\varepsilon_0!$
- Lispy high-level "personality" (but untyped)

Very simple, but little documented:

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The Commodore 64 GNU epsilon quick overview GNU epsilon Compiling epsilon Demo Challenges in targeting the 6502

# Compiling epsilon

How I compile GNU epsilon:

- Build some desired global state to compile, by successive definitions;
- Visit the reachable data graph and generate (native) data.
- Translate procedure bodies (which are also data) into native code
  - At this point procedure bodies are  $\varepsilon_0$  only easy compilation!



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6502 assembly: adding 16-bit numbers from epsilon stack

Macro parameters: .fromslot1, .fromslot2, .toslot.

```
;; Load the first operand low byte.
ldy #(.fromslot1 * 2)
lda (frame_pointer), y
;; Sum to second operand's first byte, store result.
clc ; Clear carry flag.
ldy #(.fromslot2 * 2)
adc (frame_pointer), y
ldy #(.toslot * 2)
sta (frame_pointer), y
;; Keep the carry bit and work with the high byte.
ldy #(.fromslot1 * 2 + 1)
lda (frame_pointer), y
ldy #(.fromslot2 * 2 + 1)
adc (frame_pointer), y
ldy #(.toslot * 2 + 1)
sta (frame_pointer), y
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```

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	The Commodore 64 GNU epsilon Demo	GNU epsilon and cross-compiling demo Future work
Demo		

# [demo]

This might be useful if you want to try yourself (c64 branch from epsilon git):

(e1:load "bootstrap/scheme/scratch-c64-demo.e")
(c (fio:write "foo" (i (fibo 10))))
(c (test-sprites-interactively))

acme -format cbm -o /tmp/q /tmp/q.a && x64 /tmp/q



The Commodore 64 GNU epsilon and cross-compilir Demo

- Make an official epsilon release (including Commodore 64 support);
- Don't compile procedure bodies as data;
  - ...this saves a whopping 30KB on my sprite example!
- Use zeropage "registers":
  - requires compiler work;
- Possibly make a real, elaborate Commodore 64 game.



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GNU epsilon and cross-compiling demo Future work



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Any questions?



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